

Mounting the ASI + IP output board into STB

First it is necessary to consider if there is enough space to mount the board into the STB housing. Also please remember about holes for the output connectors. The board should be placed so that the connecting wires will be as short as possible, all wires should be of the same length (approximately).



ASI-IP module parralel TS in



Fig.1. Numbering of ASI + IP output board connector pins. First pin has square pad, other pads are circles (see bottom side)

After locating the place for the board, you can start soldering. To reduce the noise influence it is necessary to place ground wires of 1 to 23 contacts between the signal wires of 2 to 24 contacts, and ground wires must be connected together at one point to a ground at the STB board as shown at Fig.2. Choice of the connection point to the ground of the STB depends on the particular STB. Preferably, the connection point should be near a receiver's tuner. If you have

chosen a wrong point, a lot of errors can appear in the stream. In this case it is necessary to choose other point checking quality of output signal.



Fig.2.

ASI + IP output board is powered by STB power supply (9...27V), see Fig. 3.





If ASI+IP board is mounted properly, it starts outputting the ASI stream right away.





Fig. 4. ASI+IP board is mounted into STB

Connecting the ASI+IP output board to STB can be done in two ways depending on what program (FTA or encrypted) need to be converted in ASI/IP format:

- 1. In case if FTA program is received, ASI+IP output board should be connected to the STB's demodulator (tuner) contacts.
- 2. When encrypted program is received, ASI+IP output board should be connected to the CAM module contacts (Common Interface).

Connecting the ASI+IP output board to the STB's tuner

Different tuners can have different output connector pin assignment. The pins assignment can be found in the tuner documentation or identified by means of an oscilloscope.

If you need to identify pins assignment by means of an oscilloscope, follow the steps below:

- Use a two-channel oscilloscope;
- Check the tuner output connector pins and find SYNC signal (periodic single pulse);
- Synchronize to SYNC signal;
- Look signals at other tuner output pins:

Beginning of VALID signal coincides with beginning of SYNC.

Data signals value should be as indicated at Fig.5 (when SYNC=1). First byte of data should be equal 47hex or 71 decimal or 01000111 binary. Other bytes are in random order.



Fig. 5.

Connecting the ASI+IP output board to Common Interface (CI).

Mounting the ASI +IP output board should be done in accordance with the schematics (see Fig 6) by skilled technician.

1	9		HOWAL	62 VALID	24 23
2	9 MIVAL MCLKI		MOVAL	57 FCLK	SYNC 22 0 21
4	MCLKI MISTRT		MCLKO MOSTRT	63 SYNC	VALID 20 19
4	-		MDOOD	64 DAT0	FCLK 18 17
4	8 MDI00		MDO00	65 DAT1	DAT7 16 15
4	MDI02		MDO02	66 DAT2	DAT6 14 13
5	MDI02		MDO02	37 DAT3	DAT5 12 0 11
5	MDI04		MDO04	38 DAT4	DAT4 10 9
0	4 MDI05		MDO05	39 DAT5 40 DAT6	DAT3 8 7
0	8 MD100 9 MD101 0 MD102 3 MD103 4 MD104 5 MD105 6 MD106 6 MD107		MDO06		DAT2 6 5 DAT1 4 3
0	MDI07	2.1	MDO07	41 DAT7	DAT1 4 3 DAT0 2 0 1
2	- A A A A A A A A A A A A A A A A A A A		COMP LODGE	30	DATU 200
2	A00	1.1	D00	31	
2	A01	2.2	D01	32	GND
2	6 A02	1.1	D02	2	GND
2	5 A03		D03	3.	GND to be connected
2	4 A04	1.1	D04	4	ond to be connected
2	29 A00 227 A01 26 A02 25 A02 24 A02 24 A05 22 A04 23 A06 22 A07 11 A08 8 A09 10 A11 21 A12 13 A13 4 A13 4 A13	H	D05	5	to receiver's ground
2	2 A06	XCIF	D06	6	to receiver b ground
1	2 A07 A08		D07		
1	A08 A09		VPP1	_18	
	8 A10		VPP2	52	
1	A11		VS1#	43	
- 2	A12		CD1#	36	
1	A13	2.2	CD2#	67	
	4 A14		CE1#	42	
	7		CE2#		
	7 VCC		OF#	a	



Fig.7. Numbering of CAM Connector pins (bottom side)

It is necessary to pay attention that in some STB ASI +IP Out board will output ASI stream if a smart card is inserted in CAM and encrypted program is selected. In this case there will be decrypted by CAM programs as well as all other programs including FTA in the ASI stream. In case if FTA program is selected, streams will not appear at ASI and IP outputs.